

8V Input, Low Quiescent Current Linear Regulator

UM1550S SOT23-3
UM1550Y SOT89-3
UM1550B SOT89-3
UM1550DA DFN6 2.0×2.0
UM1550DB DFN4 1.0×1.0
UM1560S SOT23-5
UM1560DA DFN6 2.0×2.0
UM1560DB DFN4 1.0×1.0

General Description

The UM1550/1560 series is a set of high input voltage low quiescent current regulator implemented in CMOS technology. They can deliver 250mA output current and allow an input voltage as high as 8V. They are available with several fixed output voltages ranging from 1.2V to 5.0V. CMOS technology ensures low voltage drop and low quiescent current.

The UM1550 is available in SOT23-3, SOT89-3, DFN6 2.0×2.0 and DFN4 1.0×1.0 packages. The UM1560 is available in SOT23-5, DFN6 2.0×2.0 and DFN4 1.0×1.0 packages.

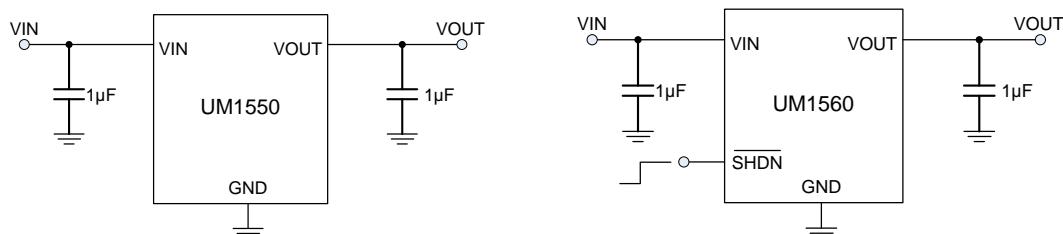
Applications

- Communication Equipments
- Audio/Video Equipments
- Portable Games
- Portable AV Equipments
- Battery-Powered Equipments

Features

- Input Voltage Range: 1.8V to 8V
- 250mA Guaranteed Output Current
- Low Dropout Voltage:
250mV (Typical) at 200mA
- Low Quiescent Current:
2.5 μ A@ $V_{IN}=8V$ (Typical)
- Low Noise: 115 μ V_{RMS} (10Hz to 100kHz)
- Available Fixed Output Voltage from 1.2V to 5.0V with 0.1V Step
- With Shutdown Control (UM1560)
- Output Current Limit
- Low Profile SOT23-3, SOT23-5, SOT89-3, DFN6 2.0×2.0 and DFN4 1.0×1.0 Packages

Typical Application Circuits



Pin Configurations
Top View

<p>UM1550S</p>	<p>UM1550S M: Month Code SOT23-3</p>
<p>UM1550Y</p>	<p>UM1550Y M: Month Code SOT89-3</p>
<p>UM1550B</p>	<p>UM1550B M: Month Code SOT89-3</p>
<p>(Top View)</p> <p>UM1550DA</p>	<p>UM1550DA M: Month Code DFN6 2.0×2.0</p>
<p>(Top View)</p> <p>UM1550DB</p>	<p>UM1550DB M: Month Code DFN4 1.0×1.0</p>

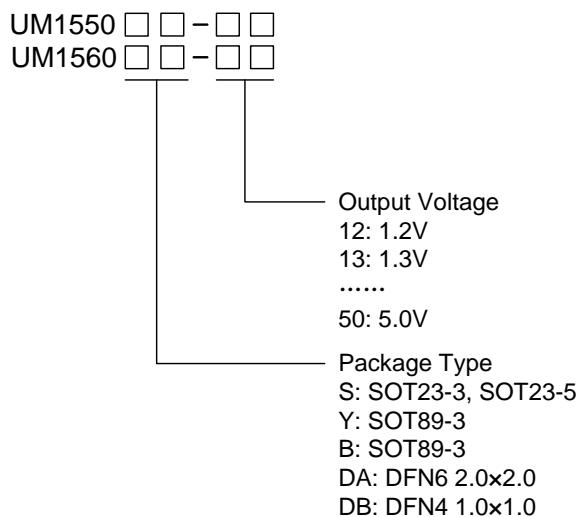
Pin Configurations
Top View

<p>UM1560S</p>	<p>UM1560S M: Month Code SOT23-5</p>
<p>(Top View)</p> <p>UM1560DA</p>	<p>UM1560DA M: Month Code DFN6 2.0×2.0</p>
<p>(Top View)</p> <p>UM1560DB</p>	<p>UM1560DB M: Month Code DFN4 1.0×1.0</p>

Pin Description

Pin Name	Pin Function
SHDN	Shutdown Control Input: High=Active LDO, Low=Shutdown LDO
GND	Ground
VIN	Supply Input
VOUT	Voltage Regulated Output
NC	Not Connected

Ordering Information



Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V _{IN}	Supply Voltage on VIN Pin	-0.3 to +10	V
V _{OUT}	Voltage on VOUT Pin	-0.3 to +10	V
T _J	Operating Junction Temperature (Notes 2, 3)	-40 to +125	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _L	Lead Temperature for Soldering 10 seconds	+260	°C
P _D (Notes 4)	Power Dissipation@25 °C	0.40 1.0 0.43 0.9 0.4	W
θ _{JA}	Package Thermal Resistance	250 100 230 110 250	°C/W

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: The UM1550/1560 is tested and specified under pulse load conditions such that T_J≈T_A. Specifications over the –40 °C to 125 °C operating junction temperature range are assured by design, characterization and correlation with statistical process controls.

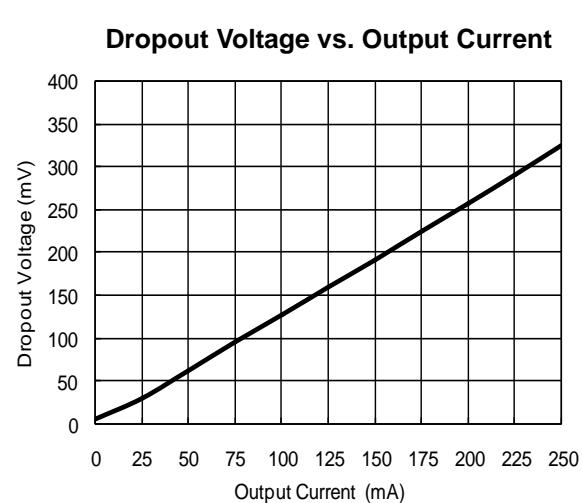
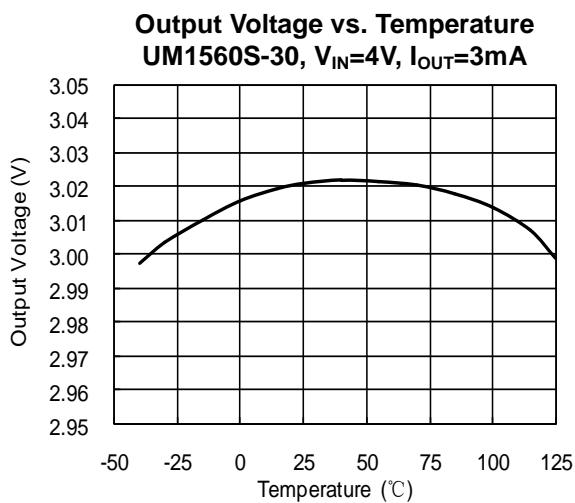
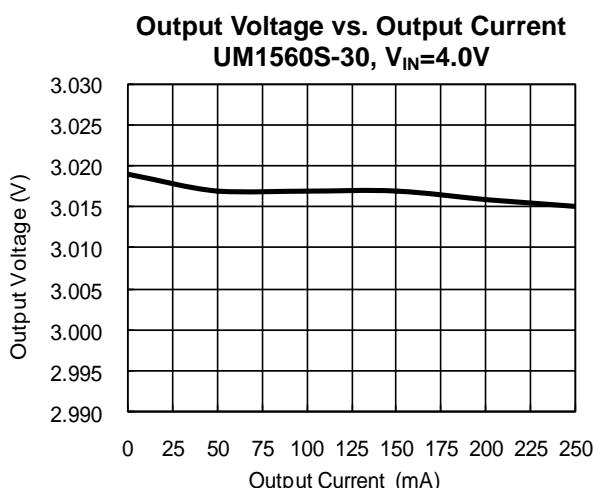
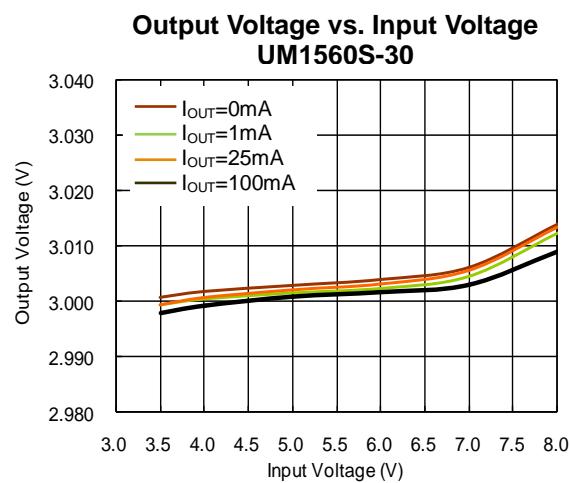
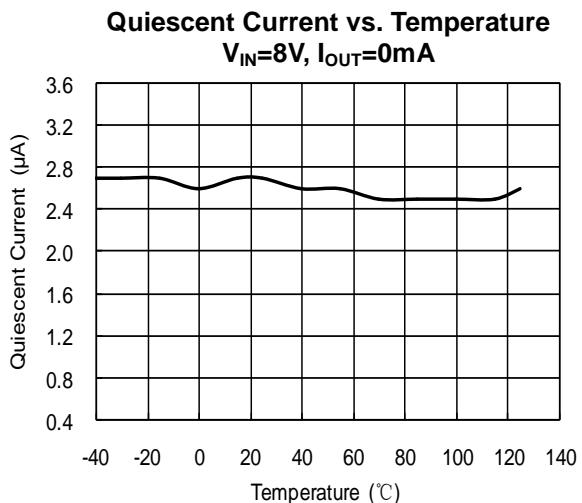
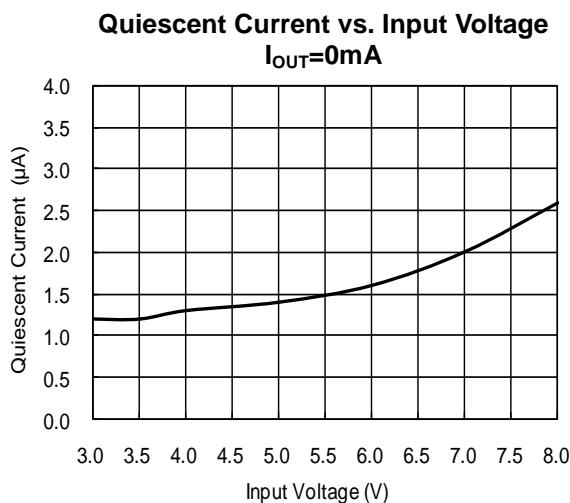
Note 3: This IC includes over temperature protection that is intended to protect the device during momentary overload conditions. Junction temperature will exceed 125 °C when over temperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.

Note 4: The maximum allowable power dissipation of any T_A (ambient temperature) is P_{DMAX}= (T_{JMAX}-T_A)/θ_{JA}. Exceeding the maximum allowable power dissipation will result in excessive die temperature and the regulator will go into thermal shutdown.

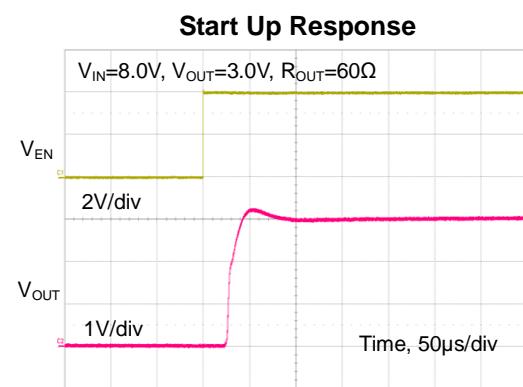
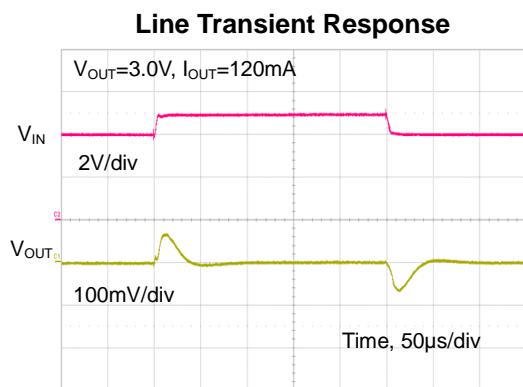
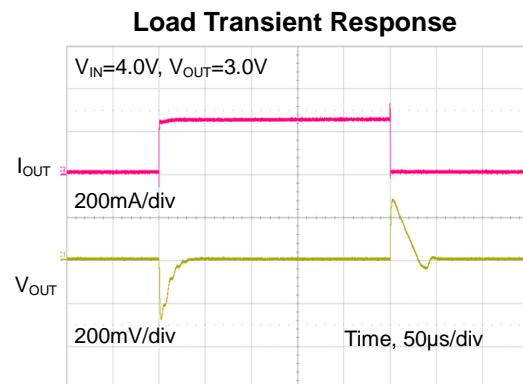
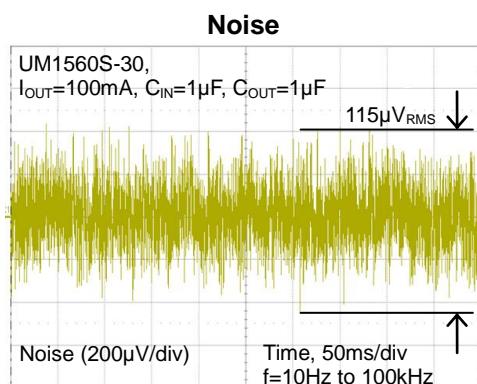
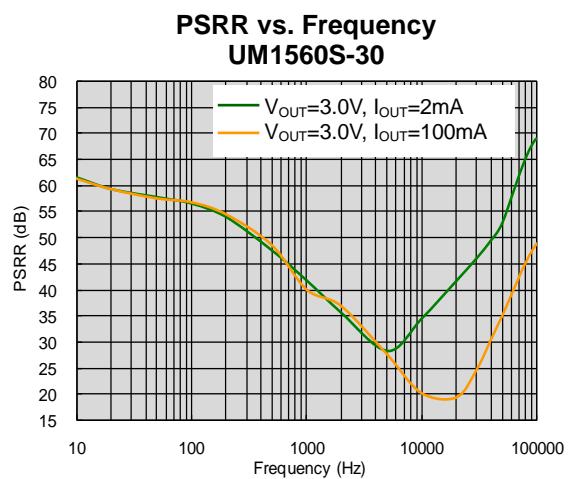
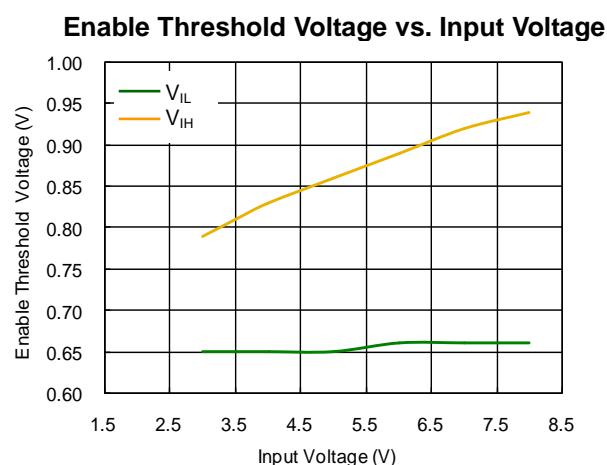
Electrical Characteristics

$V_{IN} = V_{OUT} + 1V$, $C_{IN} = C_{OUT} = 1\mu F$, $T_A = 25^\circ C$.

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
V_{IN}	Input Voltage Range			1.8		8	V
V_{OUT}	Output Voltage Range			1.2		5.0	V
I_Q	Operating Quiescent Current	$V_{IN}=8V$, $I_{OUT}=0mA$			2.5	3.5	μA
	Shut Down Quiescent Current	$V_{IN}=8V$, $\overline{SHDN}=GND$				1	μA
$I_{\overline{SHDN}}$	\overline{SHDN} Input Current	$\overline{SHDN}=V_{IN}$ or GND				1	μA
I_{OUT}	Output Current			250			mA
	Output Voltage Accuracy	$1mA \leq I_{OUT} \leq 250mA$		-2.5		+2.5	%
ΔV_{DO}	Dropout Voltage	$I_{OUT}=200mA$			250	330	mV
I_{LIMIT}	Output Current Limit	$R_L=1\Omega$		280	360	500	mA
t	Startup Time Response	$V_{OUT}=3.3V$, $R_L=68\Omega$, $C_{OUT}=1\mu F$			80		μs
V_{IL}	\overline{SHDN} Input Low Voltage	$V_{IN}=1.8V$ to 8V				0.4	V
V_{IH}	\overline{SHDN} Input High Voltage	$V_{IN}=1.8V$ to 8V		1.2			V
	Output Voltage TC	-40 °C to +125 °C			60		ppm/ °C
	Line Regulation	$V_{OUT}+0.3V \leq V_{IN} \leq 8.0V$ or $V_{IN}>1.8V$ $I_{OUT}=10mA$				0.3	%/V
	Load Regulation	$V_{IN}=V_{OUT}+1V$ or $V_{IN}>1.8V$ $1mA \leq I_{OUT} \leq 250mA$				0.6	%
	Output Voltage Noise	$V_{OUT}=3.0V$, $f=10Hz$ to 100kHz, $C_{IN}=1\mu F$, $I_{OUT}=100mA$			115		μV_{RMS}
PSRR	Power Supply Ripple Rejection	$V_{IN}=V_{OUT}+1V$ $I_{OUT}=100mA$	$f=100Hz$		56		dB
			$f=1kHz$		42		
	ESD Rating	Human Body Mode		2			kV

Typical Operating Characteristics (shown for 3.0V output option)
 $(C_{IN}=1.0\ \mu F, C_{OUT}=1.0\ \mu F, T_A=25\ ^\circ C,$ unless otherwise specified.)


Typical Operating Characteristics (Continued) (shown for 3.0V output option)
 (C_{IN}=1.0 µF, C_{OUT}=1.0 µF, T_A=25 °C, unless otherwise specified.)



Package Information

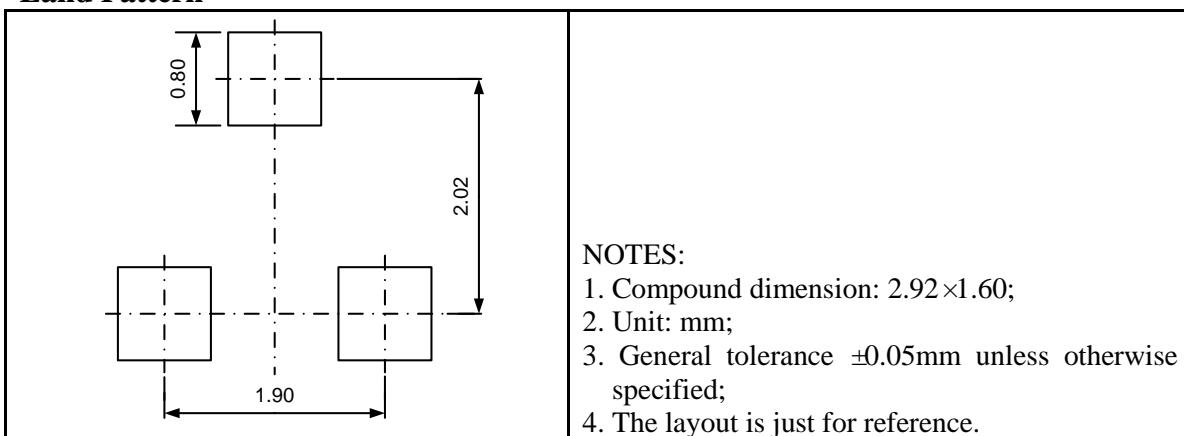
UM1550S: SOT23-3

Outline Drawing

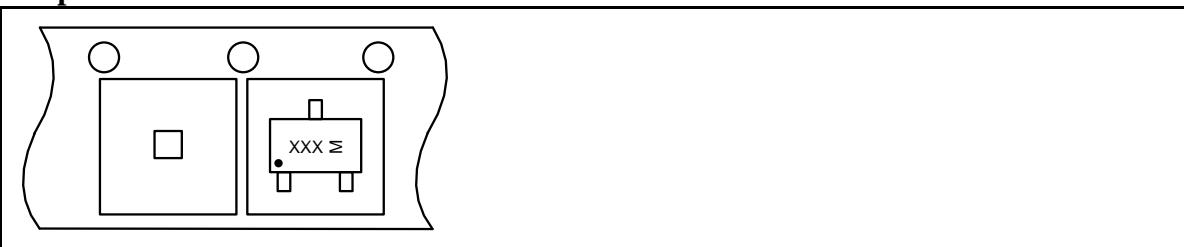
Symbol	DIMENSIONS			
	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.05	1.25	0.041	0.049
A1	0.00	0.10	0.000	0.004
A2	1.05	1.15	0.041	0.045
b	0.30	0.50	0.012	0.020
c	0.10	0.20	0.004	0.008
D	2.82	3.02	0.111	0.119
E	1.50	1.70	0.059	0.067
E1	2.65	2.95	0.104	0.116
e	0.95REF		0.037REF	
e1	1.80	2.00	0.071	0.079
L	0.55REF		0.022REF	
L1	0.30	0.60	0.012	0.024
θ	0°		8°	

The outline drawing includes three views: Top View, End View, and Side View. The Top View shows a rectangular package with lead positions 1 and 2, and dimensions D, E1, E, b, c, h, e, e1, and L. The End View shows the lead profile with height θ and width 0.2. The Side View shows the thickness A1 and lead spacing A2.

Land Pattern



Tape and Reel Orientation

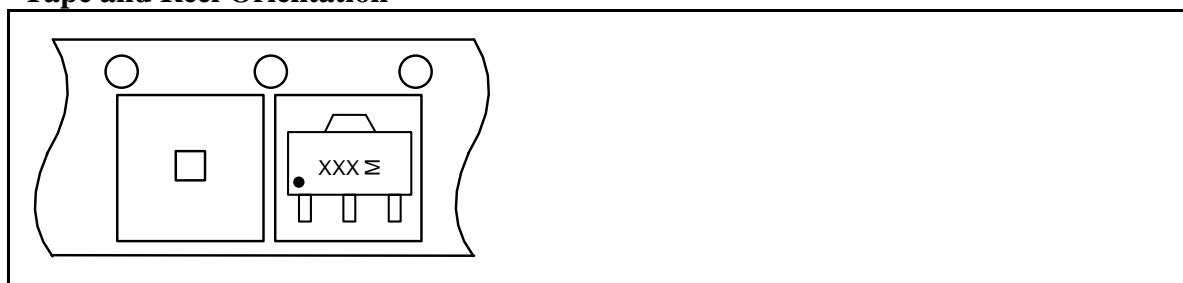


UM1550Y, UM1550B: SOT89-3
Outline Drawing

Symbol	DIMENSIONS			
	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.40	1.60	0.055	0.063
b	0.32	0.52	0.013	0.020
b1	0.40	0.58	0.016	0.023
c	0.35	0.44	0.014	0.017
D	4.40	4.60	0.173	0.181
D1	1.55REF		0.061REF	
E	2.30	2.60	0.091	0.102
E1	3.94	4.25	0.155	0.167
e	1.50TYP		0.06TYP	
e1	3.00TYP		0.118TYP	
L	0.90	1.20	0.035	0.047

Land Pattern

	<p>NOTES:</p> <ol style="list-style-type: none"> 1. Compound dimension: 4.50×2.45; 2. Unit: mm; 3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified; 4. The layout is just for reference.
--	--

Tape and Reel Orientation


UM1550DA, UM1560DA: DFN6 2.0×2.0

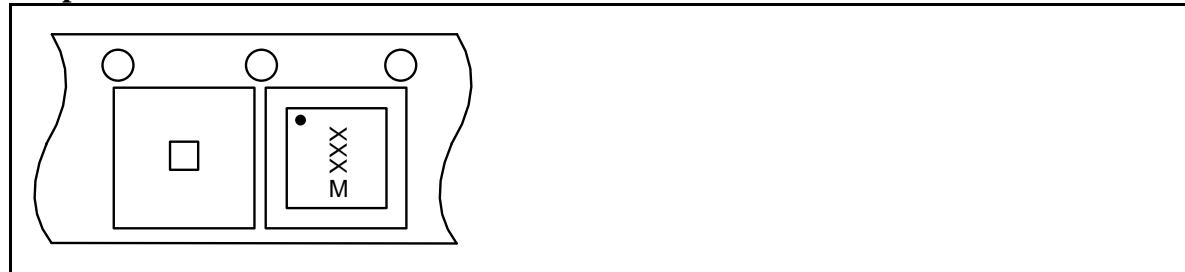
Outline Drawing

Symbol	DIMENSIONS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	0.57	-	0.80	0.022	-	0.031
A1	0.00	-	0.05	0.000	-	0.002
A3	0.20REF			0.008REF		
b	0.25	0.30	0.35	0.010	0.012	0.014
D	1.95	2.00	2.075	0.077	0.079	0.082
D2	1.05	-	1.75	0.041	-	0.069
E	1.95	2.00	2.075	0.077	0.079	0.082
E2	0.45	-	1.06	0.018	-	0.042
e	0.65BSC			0.026BSC		
L	0.25	-	0.45	0.010	-	0.018

Land Pattern

	<p>NOTES:</p> <ol style="list-style-type: none"> 1. Compound dimension: 2.00×2.00; 2. Unit: mm; 3. General tolerance ± 0.05mm unless otherwise specified; 4. The layout is just for reference.
--	---

Tape and Reel Orientation

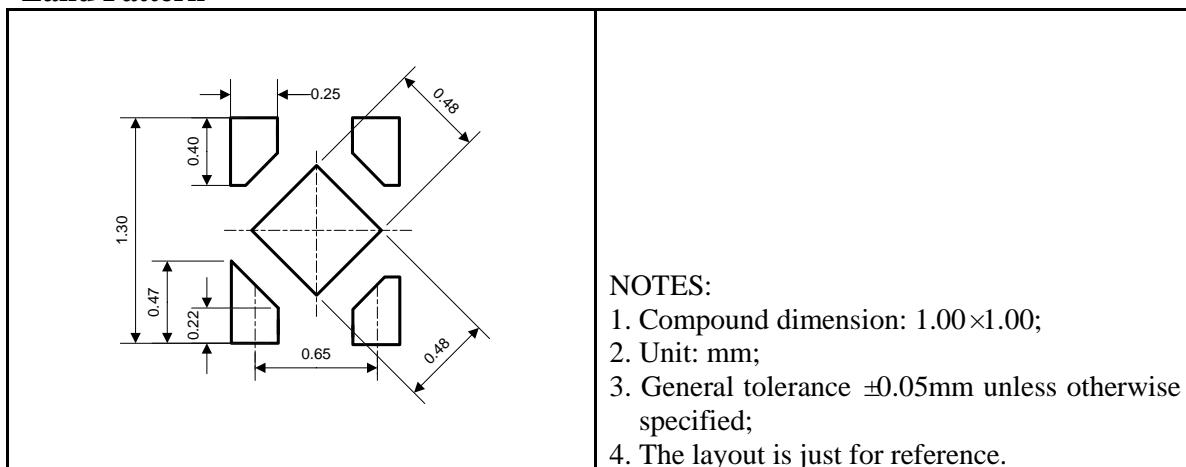


UM1550DB, UM1560DB: DFN4 1.0×1.0

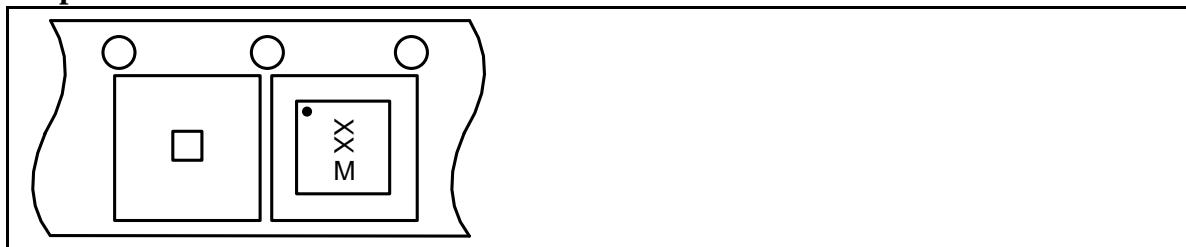
Outline Drawing

Symbol	DIMENSIONS		
	Min	Typ	Max
A	-	0.39	0.40
A1	0.00	0.02	0.05
A3	-	0.13	-
b	0.20	0.25	0.30
D	0.95	1.00	1.05
E	0.95	1.00	1.05
D2	0.38	0.48	0.58
E2	0.38	0.48	0.58
e	0.65BSC		
L	0.20	0.25	0.30
L1	0.27	0.32	0.37
L2	0.02	0.07	0.12
Z	-	0.05	-

Land Pattern



Tape and Reel Orientation



UM1560S: SOT23-5

Outline Drawing

Symbol	DIMENSIONS			
	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.05	1.25	0.041	0.049
A1	0.00	0.10	0.000	0.004
A2	1.05	1.15	0.041	0.045
b	0.30	0.50	0.012	0.020
c	0.10	0.20	0.004	0.008
D	2.82	3.02	0.111	0.119
E	1.50	1.70	0.059	0.067
E1	2.65	2.95	0.104	0.116
e	0.95REF		0.037REF	
e1	1.80	2.00	0.071	0.079
L	0.30	0.60	0.012	0.024
θ	0°		8°	

The outline drawing provides three views of the package: Top View, End View, and Side View. The Top View shows the chip layout with pins 1 through 5 labeled. The End View shows the lead profile with lead thickness 'b' and lead spacing 'e'. The Side View shows the height of the package and the lead spacing 'A1' and 'A2'.

Land Pattern

<p>The land pattern diagram illustrates the pad layout. It features two rows of pads. The top row has a pitch of 0.60 mm between pads, with a total width of 0.90 mm. The bottom row has a pitch of 0.95 mm between pads, with a total width of 2.40 mm. The distance between the center of the top row and the center of the bottom row is also 0.95 mm.</p>	<p>NOTES:</p> <ol style="list-style-type: none"> 1. Compound dimension: 2.92×1.60; 2. Unit: mm; 3. General tolerance ± 0.05mm unless otherwise specified; 4. The layout is just for reference.
---	--

Tape and Reel Orientation



GREEN COMPLIANCE

Union Semiconductor is committed to environmental excellence in all aspects of its operations including meeting or exceeding regulatory requirements with respect to the use of hazardous substances. Numerous successful programs have been implemented to reduce the use of hazardous substances and/or emissions.

All Union components are compliant with the RoHS directive, which helps to support customers in their compliance with environmental directives. For more green compliance information, please visit:

http://www.union-ic.com/index.aspx?cat_code=RoHSDDeclaration

IMPORTANT NOTICE

The information in this document has been carefully reviewed and is believed to be accurate. Nonetheless, this document is subject to change without notice. Union assumes no responsibility for any inaccuracies that may be contained in this document, and makes no commitment to update or to keep current the contained information, or to notify a person or organization of any update. Union reserves the right to make changes, at any time, in order to improve reliability, function or design and to attempt to supply the best product possible.



Union Semiconductor, Inc

Add: Unit 606, No.570 Shengxia Road, Shanghai 201210

Tel: 021-51093966

Fax: 021-51026018

Website: www.union-ic.com